

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Date STUTTARD et al.

Application No.: 09/972,797

Filed: October 9, 2001

For: PARALLEL DATA PROCESSING  
APPARATUS

)  
)  
) Group Art Unit: 2183

)  
) Examiner: Unassigned

)  
) Confirmation No.: 3642

**INFORMATION DISCLOSURE STATEMENT  
TRANSMITTAL LETTER**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Enclosed is an Information Disclosure Statement and accompanying form PTO-1449 for the above-identified patent application.

- ☒ No additional fee for submission of an IDS is required.
- ☐ The fee of \$180.00 (126) as set forth in 37 C.F.R. § 1.17(p) is also enclosed.
- ☐ A certification under 37 C.F.R. § 1.97(e) is also enclosed.
- ☐ A certification under 37 C.F.R. § 1.97(e), and the fee of \$180.00 (126) as set forth in 37 C.F.R. § 1.17(p) are also enclosed.
- ☐ Charge \$\_\_\_\_\_ to Deposit Account No. 02-4800 for the fee due.
- ☐ A check in the amount of \$\_\_\_\_\_ is enclosed for the fee due.


The Commissioner is hereby authorized to charge any appropriate fees under 37 C.F.R. §§ 1.16, 1.17 and 1.21 that may be required by this paper, and to credit any overpayment, to Deposit Account No. 02-4800. This paper is submitted in duplicate.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

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By:

  
Kenneth B. Leffler  
Registration No. 36,075

Date: June 12, 2002

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**INFORMATION DISCLOSURE STATEMENT**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

In accordance with the duty of disclosure as set forth in 37 C.F.R. § 1.56, Applicants hereby submit the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98. Pursuant to 37 C.F.R. § 1.98, a copy of each of the documents cited is enclosed.

**United Kingdom Search Report issued in connection with Application  
No. GB 9908209.1, dated November 12, 1999:**

United Kingdom Patent Publication No. GB 2287559, September 20, 1995.

European Patent Publication No. EP 0428327 A1, May 22, 1991.

U.S. Patent No. 5,828,894, issued to Wilkinson et al., October 27, 1998.

U.S. Patent No. 4,739,476, issued to Fiduccia, April 19, 1988.

U.S. Patent No. 4,590,465, issued to Fuchs, May 20, 1986.

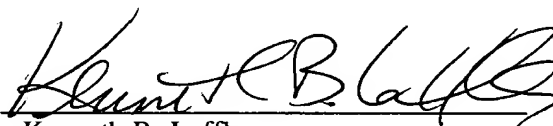
JENG et al.: "A fault-tolerant multistage interconnection network for multiprocessor systems using dynamic redundancy", 6<sup>th</sup> International Conference on Distributed Computing Systems Proceedings, pp. 70-77; see INSPEC abstract number C86052412.

The documents are being submitted within 3 months of the filing or entry of the national stage of this application or before the first Office Action on the merits, whichever is later, therefore no fee or statement is required under 37 C.F.R. § 1.97(b).

To assist the Examiner, the documents are listed on the attached form PTO-1449. It is respectfully requested that an Examiner initialed copy of this form be returned to the undersigned.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

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Date: June 12, 2002

Substitute for form 1449A/PTO

ATTORNEY'S DKT NO.  
032658-018APPLICATION NO.  
09/972,797

# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

APPLICANT  
Dave STUTTARD et al.FILING DATE  
October 9, 2001GROUP  
2183

## U.S. PATENT DOCUMENTS

Examiner Initials	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication (MM-DD-YYYY)
	Number	Kind Code (if known)		
	5,828,894		Wilkinson et al.	October 27, 1998
	4,739,476		Fiduccia	April 19, 1988
	4,590,465		Fuchs	May 20, 1986

## FOREIGN PATENT DOCUMENTS

Examiner Initials	Foreign Patent Document		Country	Date of Publication (MM-DD-YYYY)	Translation	
	Number	Kind Code (if known)			Yes	no
	GB2287559		United Kingdom	September 20, 1995		
	EP0428327A1		Europe	May 22, 1991		

## NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
	JENG et al.: "A fault-tolerant multistage interconnection network for multiprocessor systems using dynamic redundancy", 6 <sup>th</sup> International Conference on Distributed Computing Systems Proceedings, pp. 70-77; see INSPEC abstract number C86052412.

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. SEND TO: Assistant Commissioner for Patents, Washington, D.C. 20231.